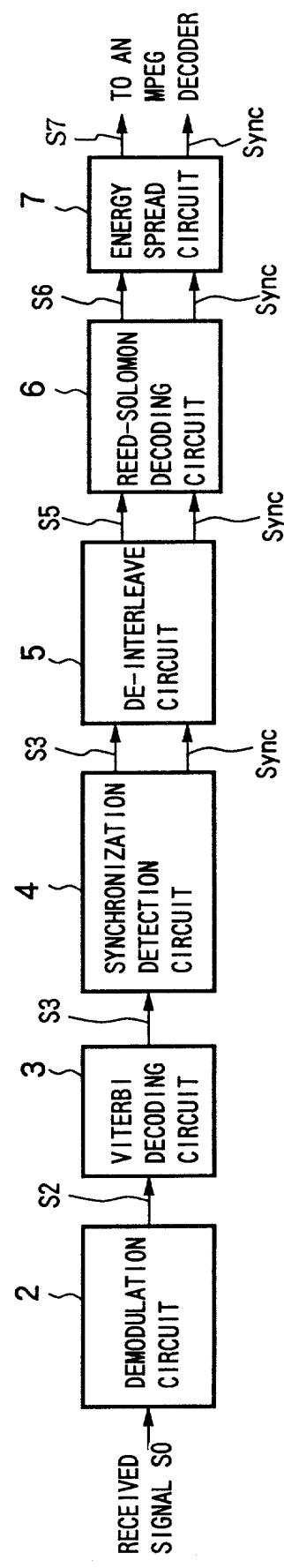
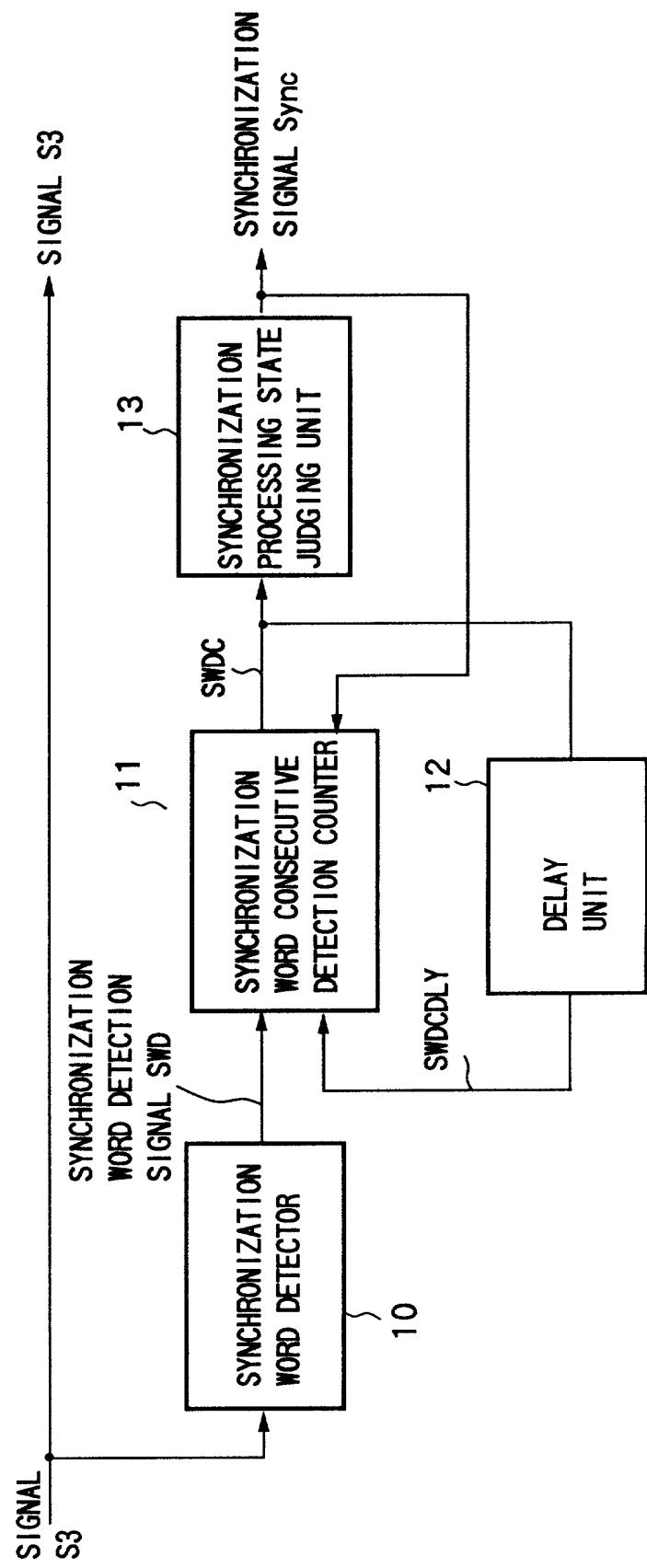


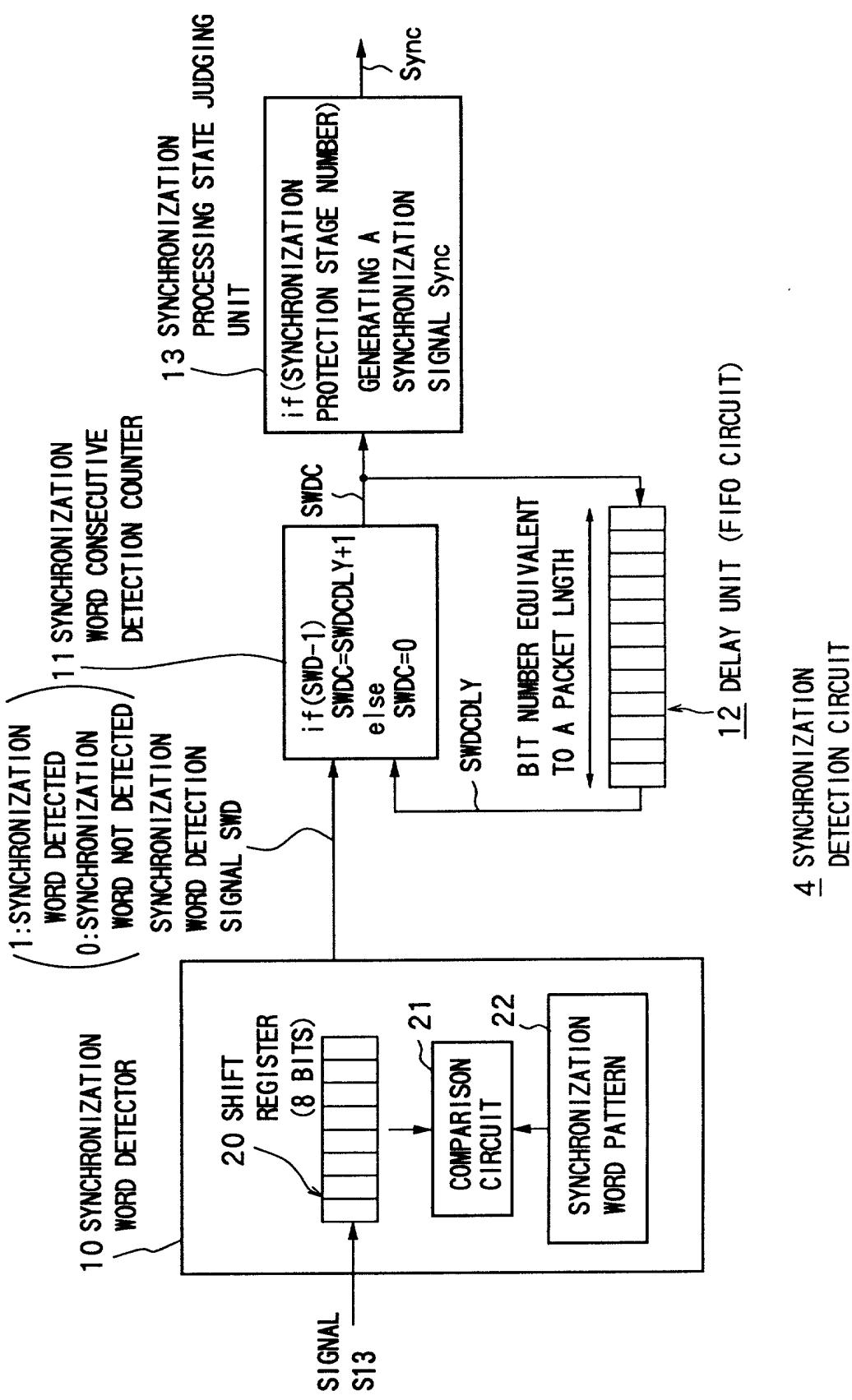
**FIG.1****1 RECEIVING APPARATUS**

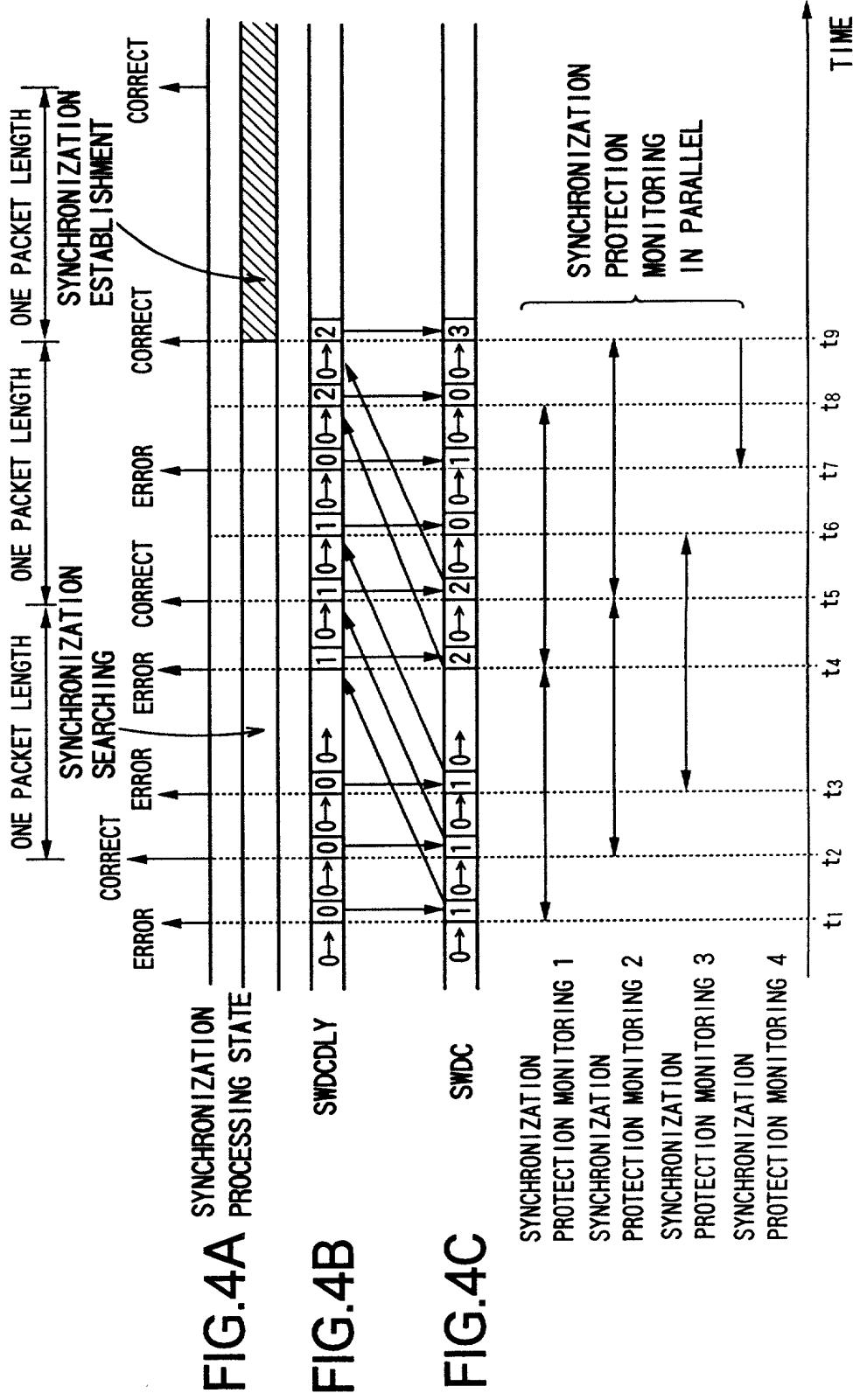
**FIG.2**



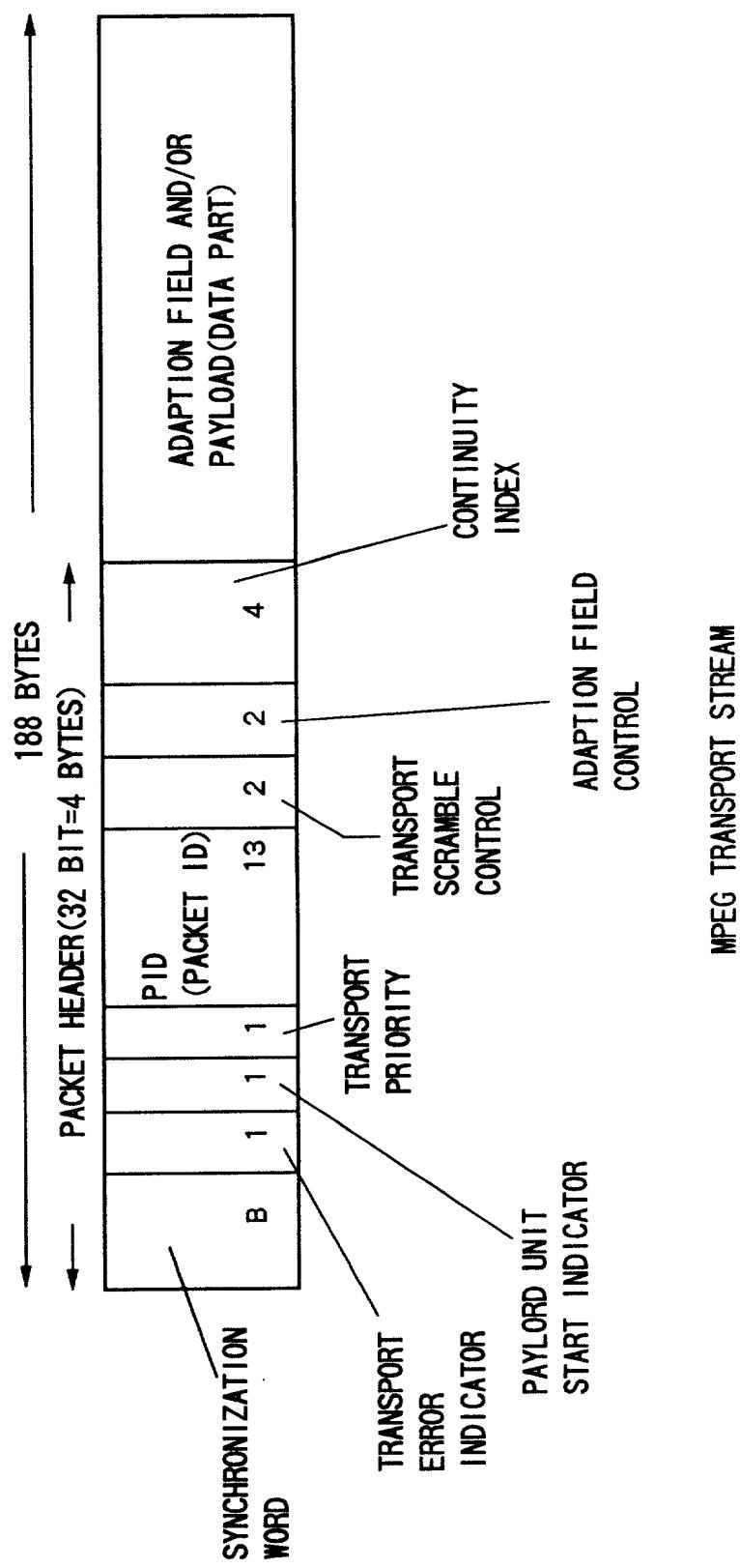
**4 SYNCHRONIZATION  
DETECTION CIRCUIT**

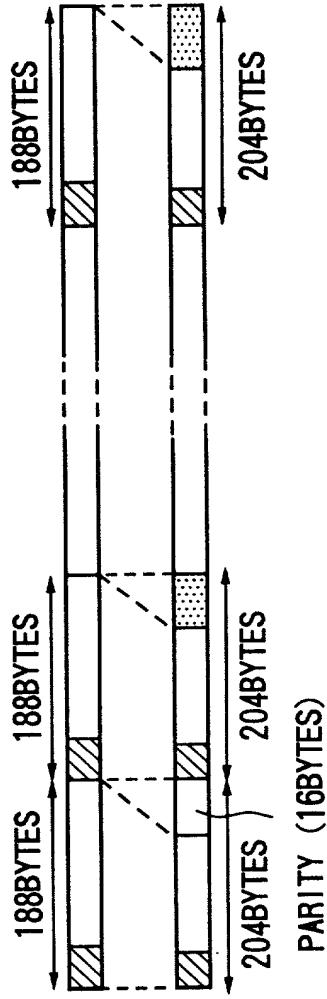
**FIG.3**





**FIG.5**





**FIG.6A** TRANSPORT  
STREAM PACKET

**FIG.6B** SLOT DATA

